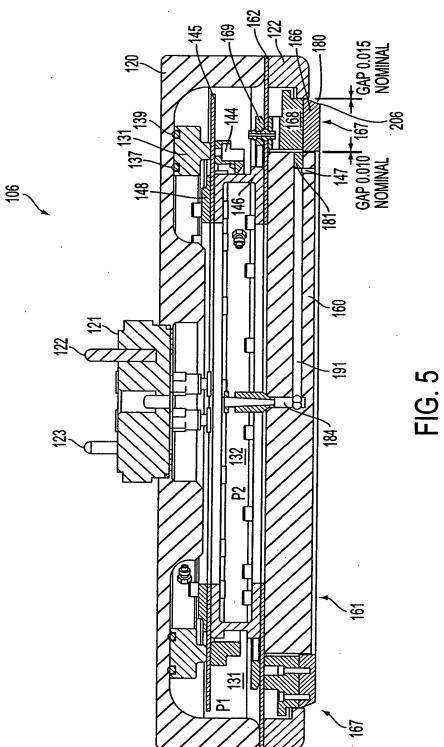
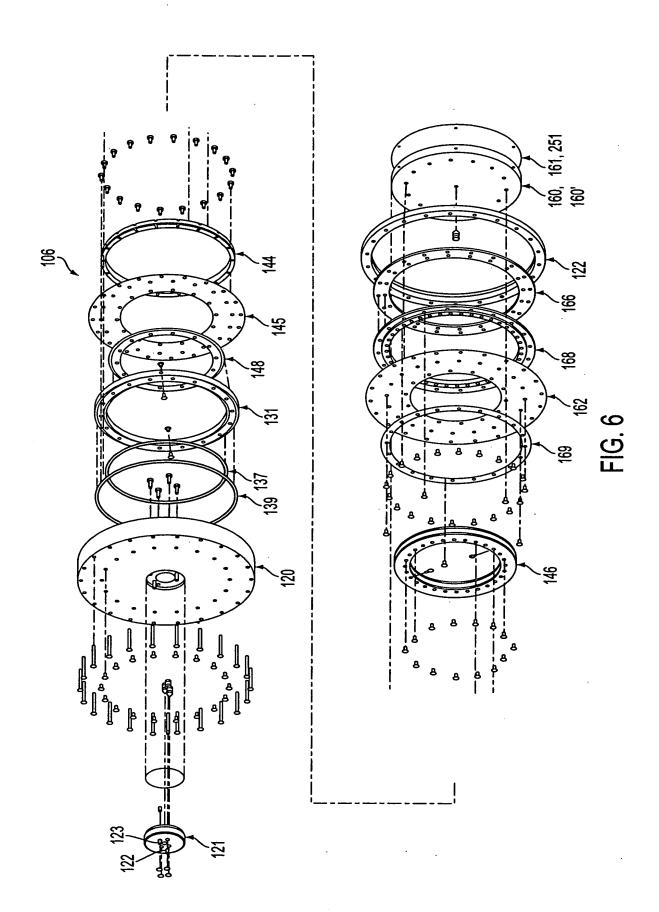


FIG. 4





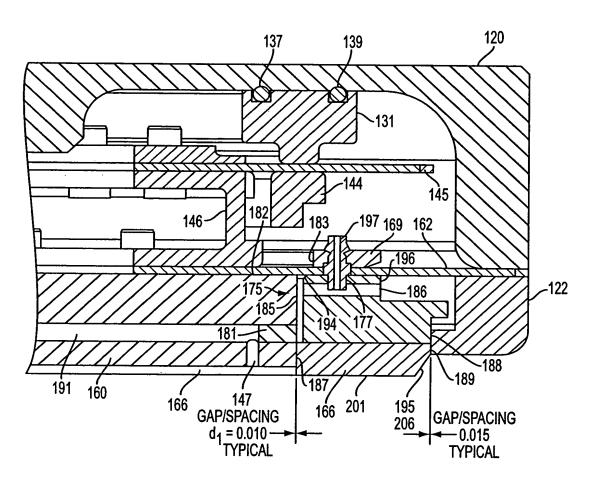


FIG. 7

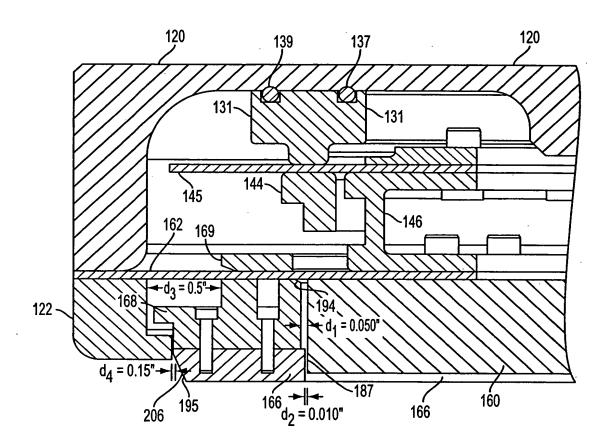
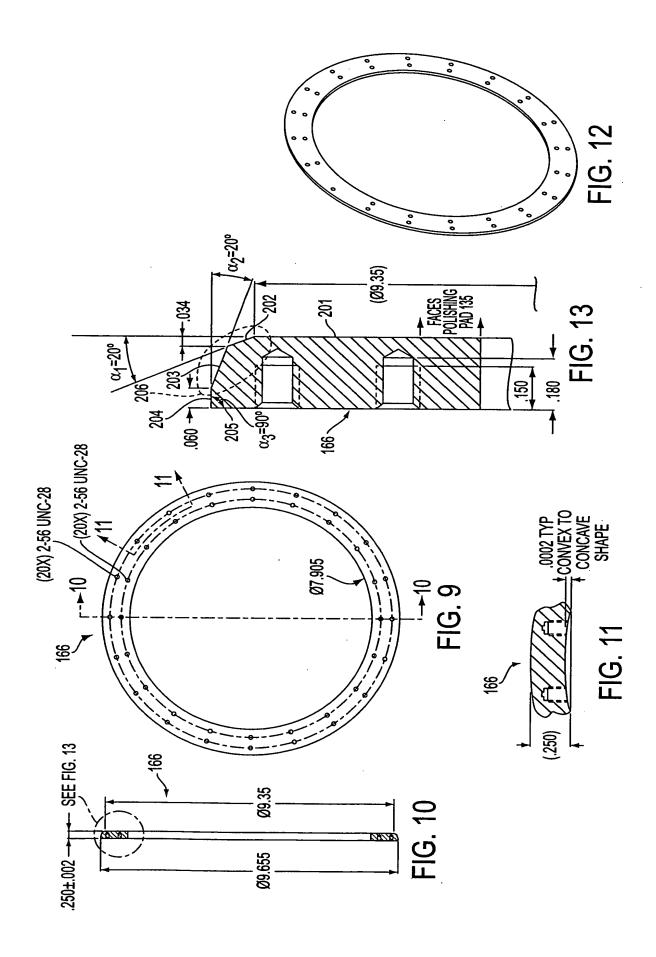
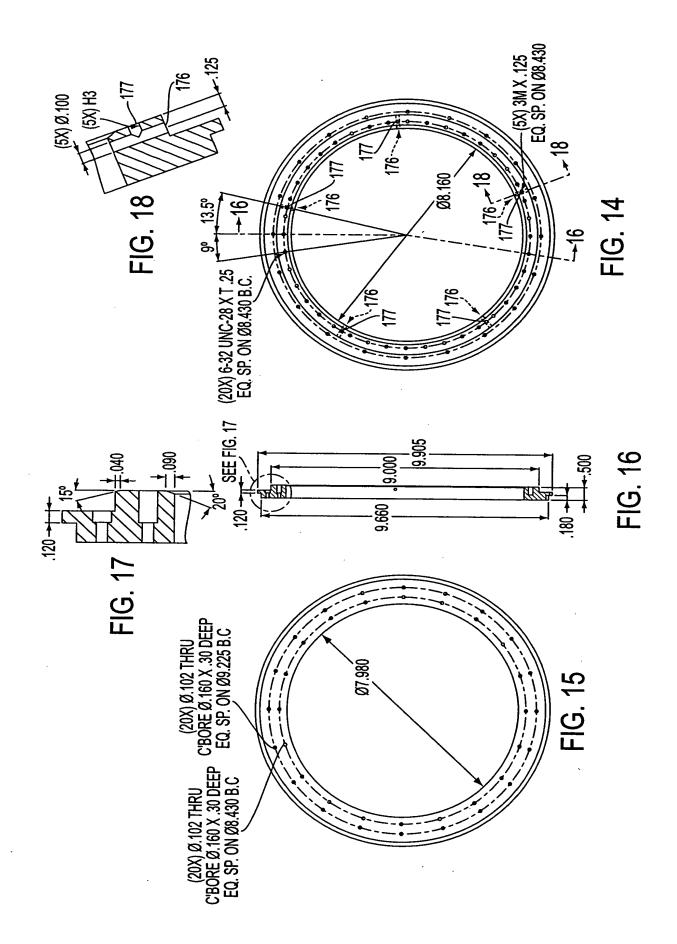
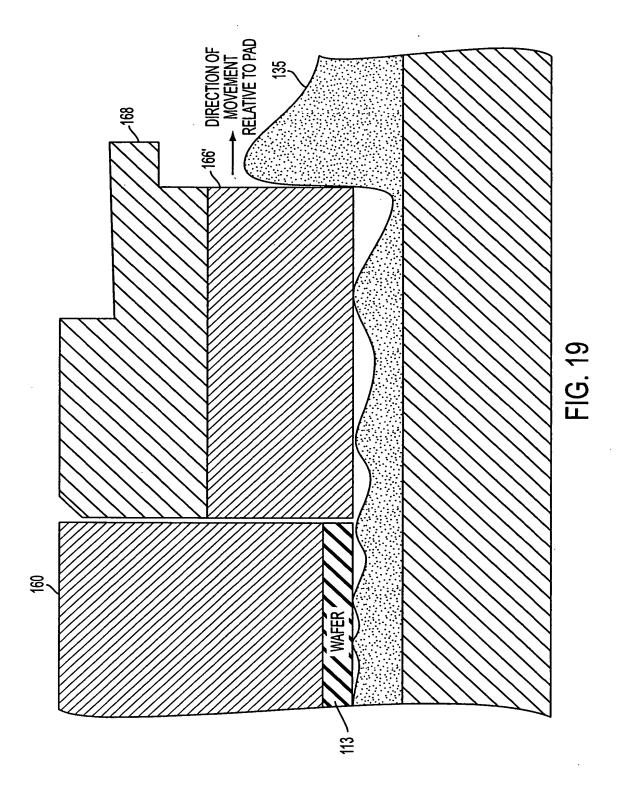
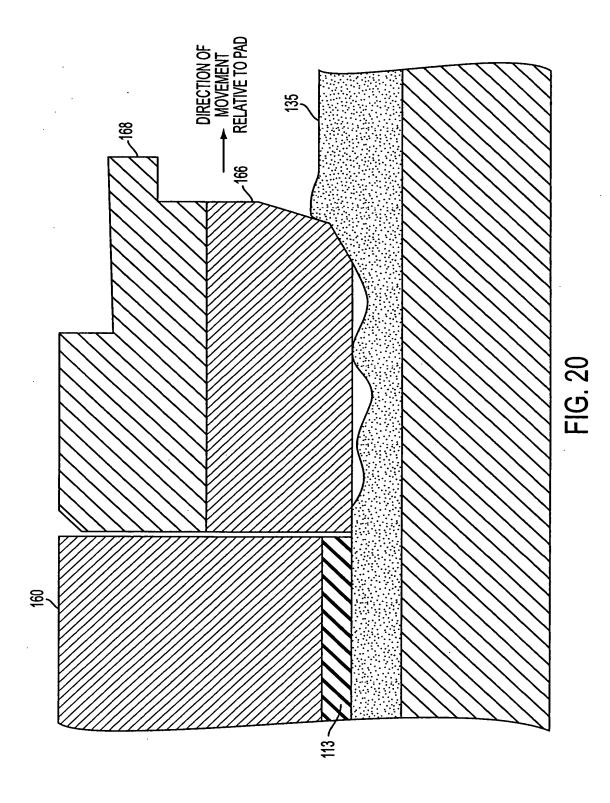


FIG. 8









## WAFER LOADING PROCEDURE

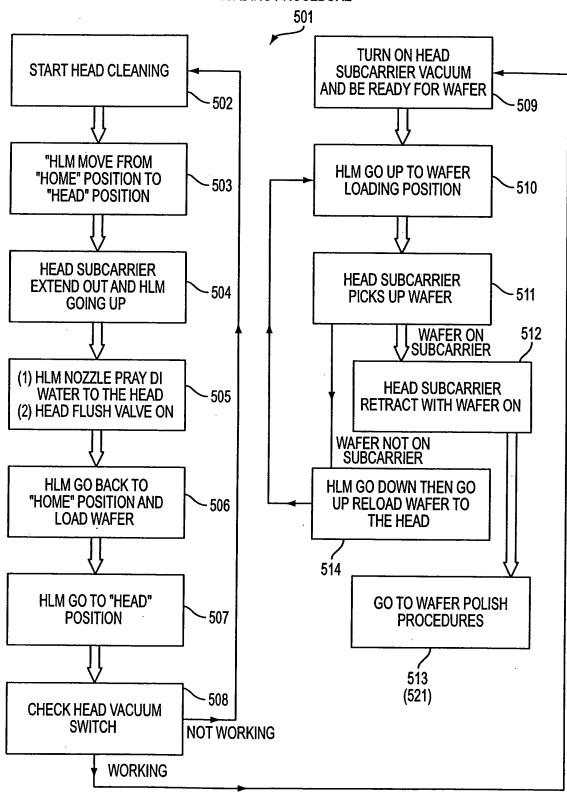
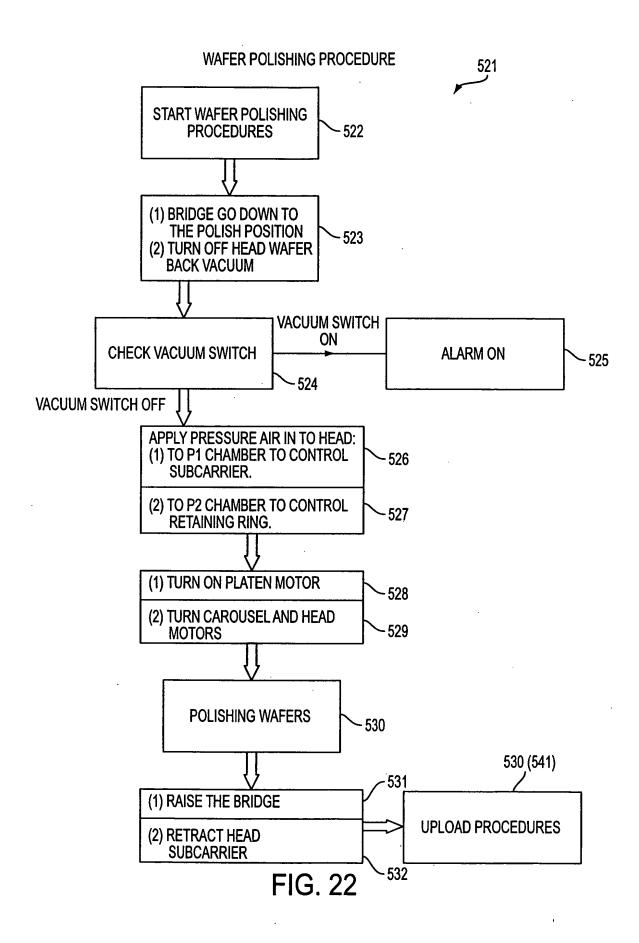


FIG. 21



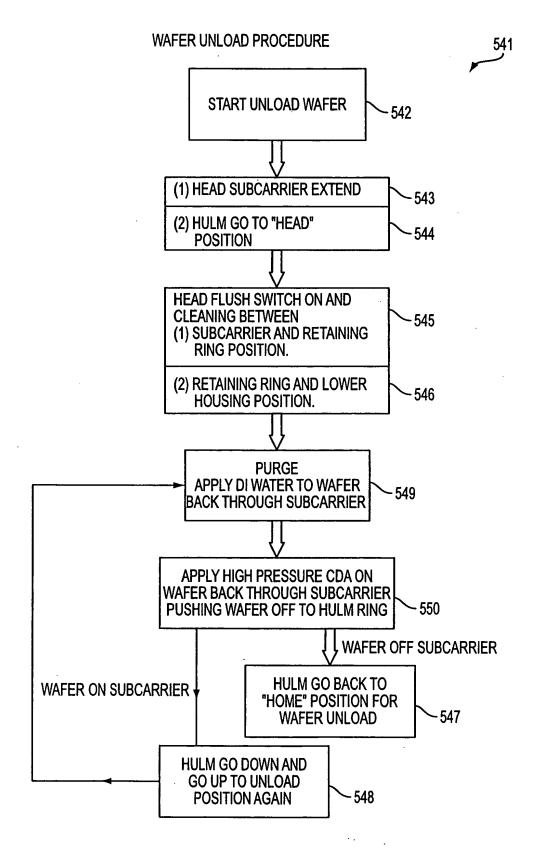


FIG. 23